

Figure 1

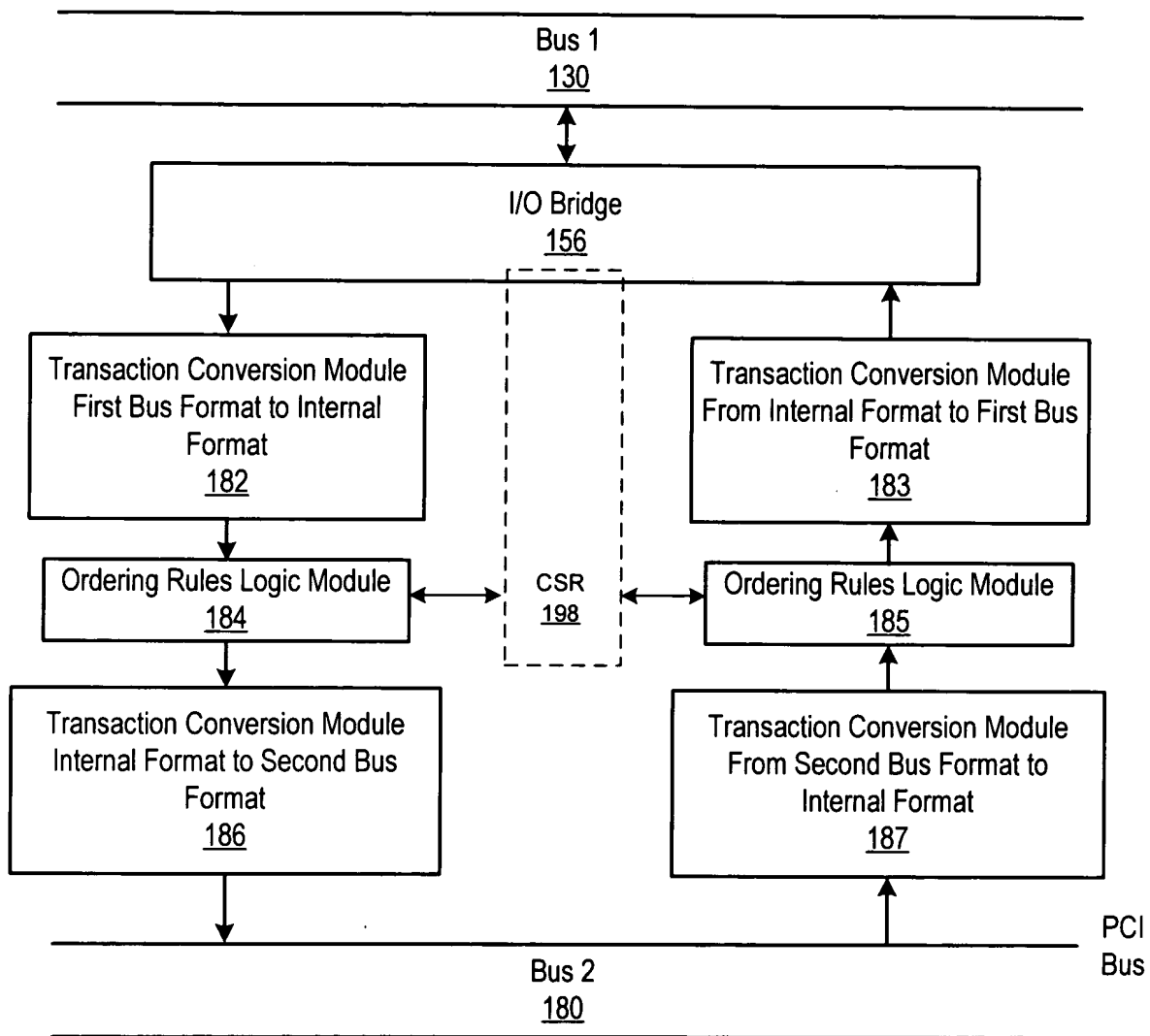


Figure 2

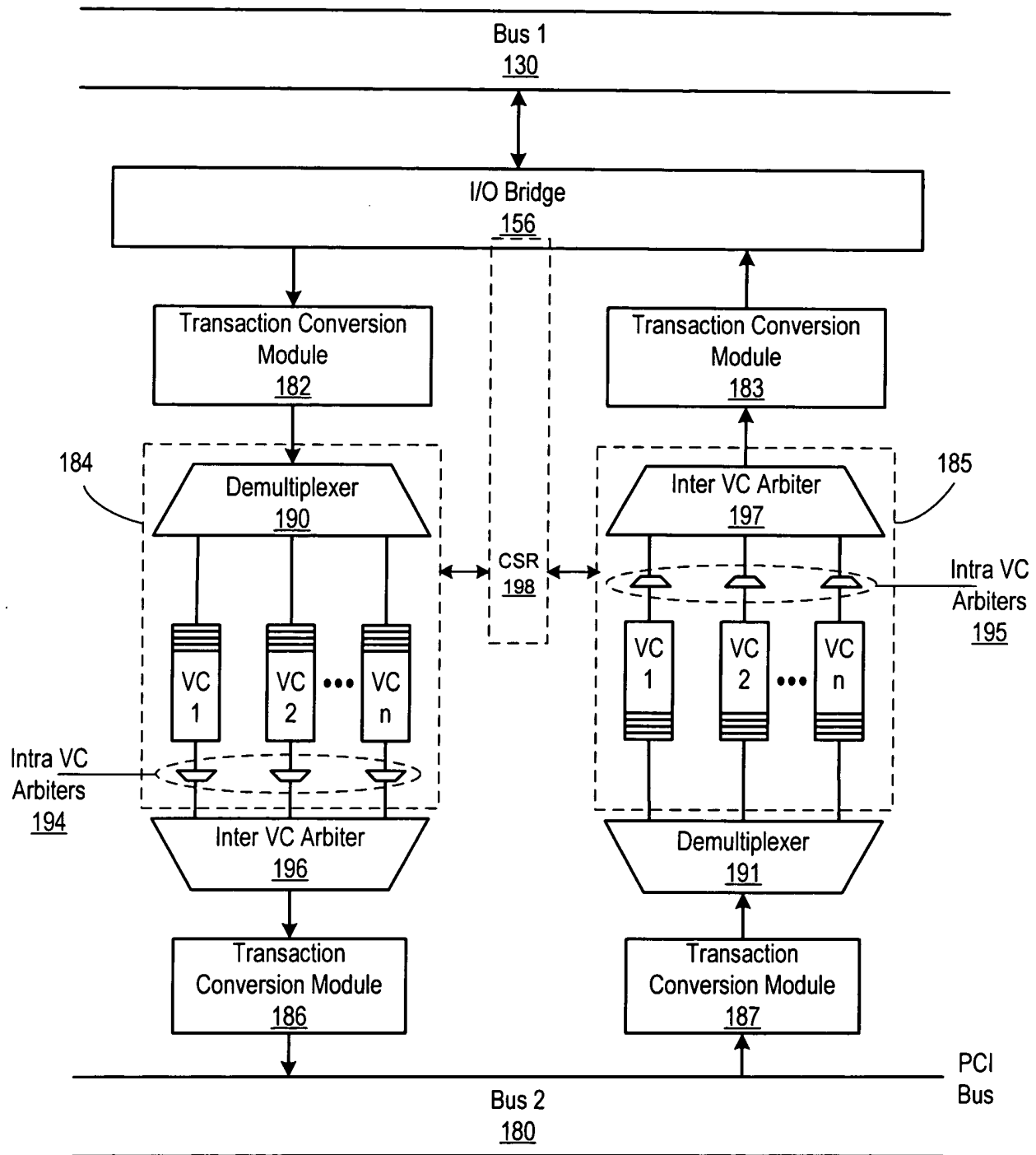


Figure 3

	VC1	VC2	...	VCn
VC1	@	Yes	...	
VC2		@	...	
...
VCn	Yes	Yes	...	@

Figure 4

	Posted transaction	Non-posted transaction	Response
Posted transaction			
Non-posted transaction	Yes (A)		
Response	Yes (B)		

Figure 5

	C1	C2	...	Cn
C1				
C2				Yes
...				
Cn				

Figure 6

	IO command from CPU	Memory command from CPU	Memory command from Non- CPU
IO Command from CPU	Yes		
Memory command from CPU			
Memory command from Non- CPU			

Figure 7